

Parametric Functional Tester

“PFT”



192-Pin Test Head

Technical Data

System

Architecture	68030 processor
User program language	GUI Test Programming Language
Operating System	Windows 98

Tester Board Set

Pin Electronics	32, 64, 96, 128, 160, 192 digital pins 1 PMU for each set of 32 pins
Bias Supply	4 bias supply pins

Specification Subset

Pin Driver	-2 V to +18 V @ +/- 30 mA
Pin Comparator	-8 V to + 18 V @ +/-1 nA
Dynamic Loads	+24 mA/-24 mA @ -2.5 V to +17.5 V
Force/Measure	

Voltage	-9 V to +19 V
Current	200 mA, 20 mA, 2 mA, 200 uA, 20 uA, 2 uA

Bias Supply

Voltage	-9V to +19 V
Current	2A, 200mA, 20mA, 2mA 200uA, 20uA, 2uA, 200nA

Configurations

Single site	32, 64, 96, 128, 160, 192 digital pins
Dual site	32, 96 digital pins